

Runtime Verification Based on Register Automata

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Register Automata

regular languages [K1951, RS1959]

- specified with finite automata, or regexp
- have finite alphabets

quasi-regular languages [KF1990]

- specified with register automata
- may have infinite alphabets

Runtime Verification for Java

- Java executions mention **values**
..., call *hasNext(231)*, return *hasNext(1)*, ...
- slicing by values [RC2012]

*HasNext(*i*)*

event *hN* = call(**hasNext()*) && target(*i*)

event *n* = call(**next()*) && target(*i*)

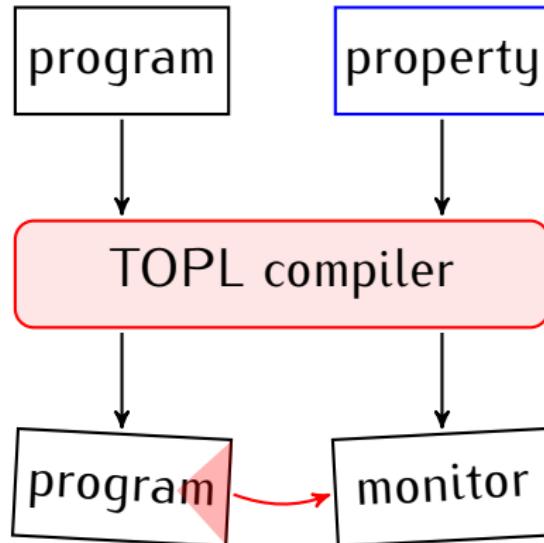
fsm : *start*[*hN* → *safe* *n* → *error*]

safe[*hN* → *safe* *n* → *start*]

DEMO

of <http://rgrig.github.com/topl>

Tool Architecture



TOPL Properties and Automata

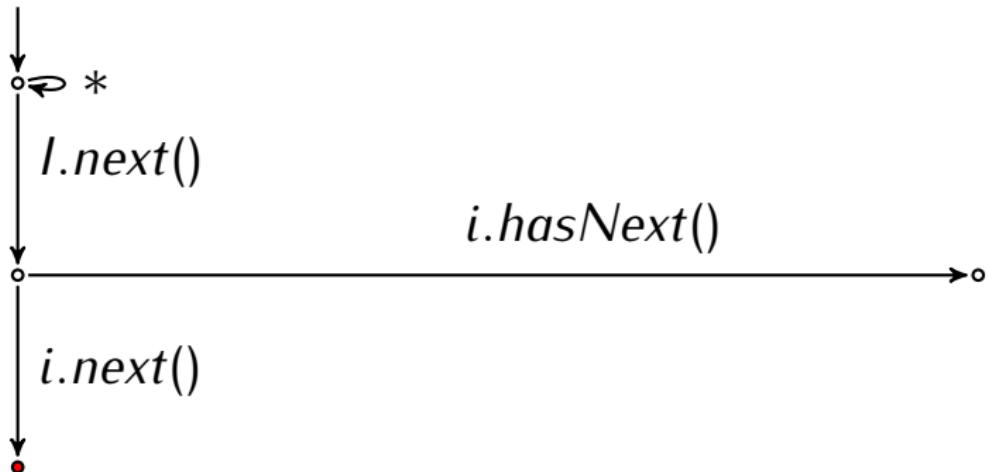
user-friendly

Given a path $start \xrightarrow{l_1} \circ \cdots \circ \xrightarrow{l_k} error$, the sequence $l_1; \dots; l_k$ of labels looks very much like a **small bad Java program**.

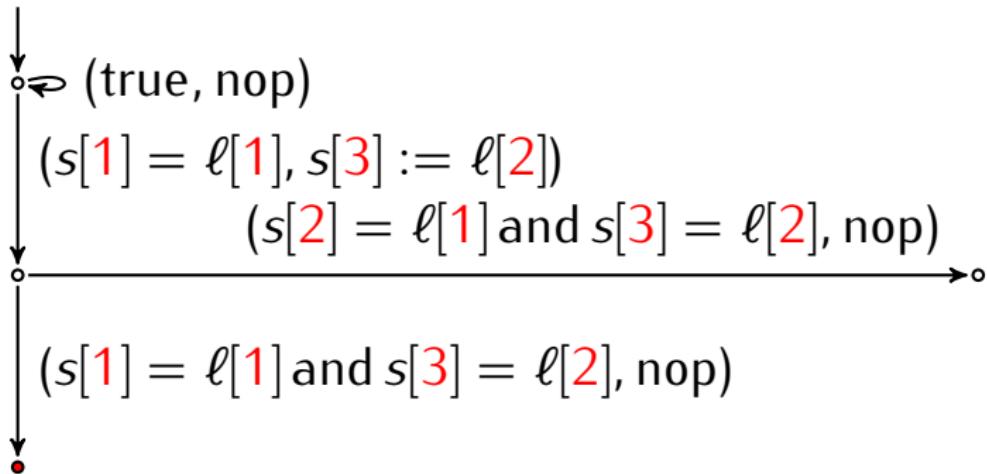
expressive

- **equivalent** to register automata
- inherits **closure** and **decidability** results

TOPL Automaton Example


$$s_0 = \{ 1 \mapsto \text{next}, 2 \mapsto \text{hasNext}, 3 \mapsto \perp \} \quad 3 = i$$
$$\tau = (\text{hasNext}, 231), (\text{next}, 231), (\text{next}, 134), \dots$$

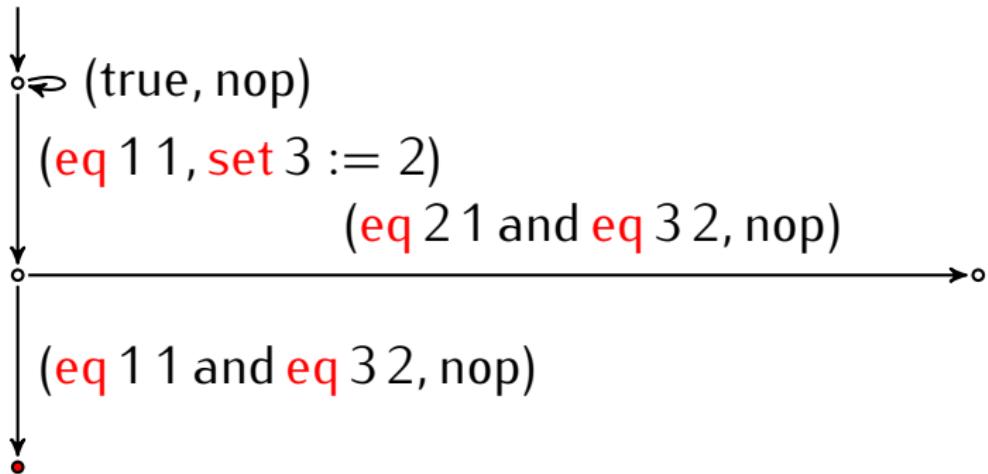
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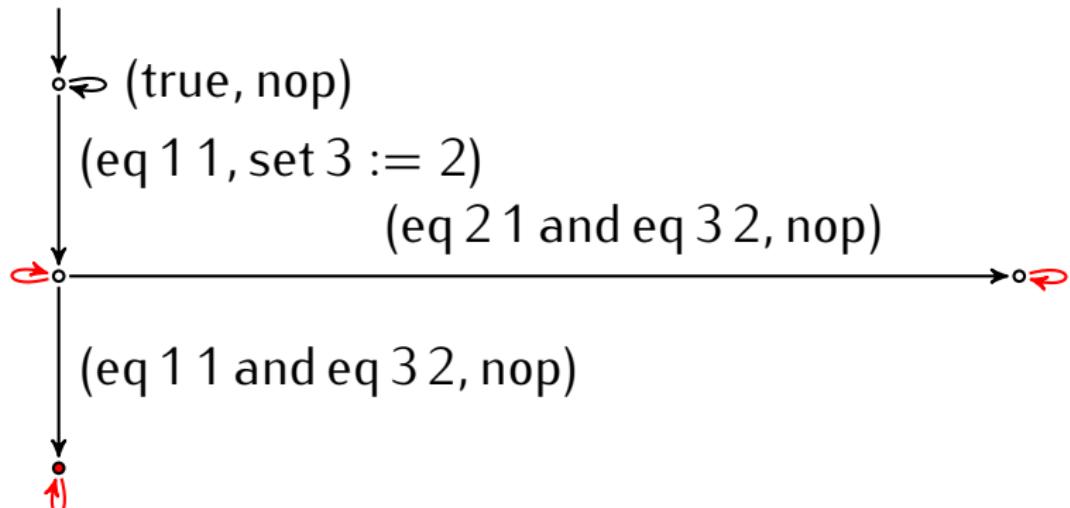
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Notations

$u, v \in V = \{0, 1, 2, \dots\}$ values

$(s_1, \dots, s_m) = s \in S = V^m$ stores

$(\ell_1, \dots, \ell_n) = \ell \in \Sigma = V^n$ letters

$g \in G$ guards

$a \in A \subseteq \Sigma \rightarrow S \rightarrow S$ actions

$\lambda \in \Lambda = G \times A$ labels

$q \in Q$ states

$(q \xrightarrow{\lambda} q') \in \delta \subseteq Q \times \Lambda \times Q$ transitions

Guards and Actions

guards

$(s, \ell) \models g$ satisfaction relation

actions

$((\text{set } i := j) \; \ell \; s)_k = (\text{if } k = i \text{ then } \ell_j \text{ else } s_k)$
 $(a_1; a_2)(\ell) = a_1(\ell) \circ a_2(\ell)$

Automata Definition and Semantics

automaton

$$(Q, q_0, \textcolor{red}{s_0}, F, \delta) \quad \delta \subseteq Q \times \Lambda \times Q$$

configuration graph

$$q \xrightarrow{(g,a)} q' \quad \text{gives} \quad (q, s) \xrightarrow{\ell} (q', s')$$

when $(s, \ell) \models g$ and $s' = a \ell s$

Register Automata Definition

- alphabet: $\Sigma = V^1$
- guards:

$$(s, v) \models \text{fresh} \iff s_i \neq v \text{ for all } i$$

$$(s, v) \models \text{eq } i \iff s_i = v$$

- labels:

(fresh, set $i := 1$) record a (locally) fresh value

(eq i , nop) test for equality with a register

TOPL Automata Definition

- guards:

$$(s, \ell) \models \text{eq } i \ j \iff s_i = \ell_j$$

$$(s, \ell) \models \text{neq } i \ j \iff s_i \neq \ell_j$$

$$\sigma \models \text{true} \iff \text{always}$$

$$\sigma \models g_1 \text{ and } g_2 \iff \sigma \models g_1 \text{ and } \sigma \models g_2$$

where σ is a pair (s, ℓ)

Results

Proposition: TOPL to RA, and back

A TOPL automaton of size N can be transformed into an equivalent RA of size $N^{O(N^2)}$. A RA of size N with $< k$ conjuncts in each guard can be transformed into an equivalent TOPL automaton of size $O(Nk^N)$.

Theorem

TOPL automata inherit all **expressivity**, **closure** and **decidability** results from register automata.

Done

- we noticed: **automata over infinite alphabets** are useful for **runtime verification**
- we designed: **TOPL automata** — tailored for Java, but equivalent to register automata
- we implemented: a **tool** for runtime verification

ToDo

- static analysis
- implementation of histories (sets) [TG2013]

References

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- KF1990 Kaminski, Francez, *Finite Memory Automata*
- A+2005 Allan et al., *Adding Trace Matching with Free Variables to AspectJ*
- RC2012 Rosu, Chen, *Semantics and Algorithms for Parametric Monitoring*
- B+2012 Barringer et al., *Quantified Event Automata: Towards Expressive and Efficient Runtime Monitors*
- TG2013 Tzevelekos, Grigore, *History-Register Automata*